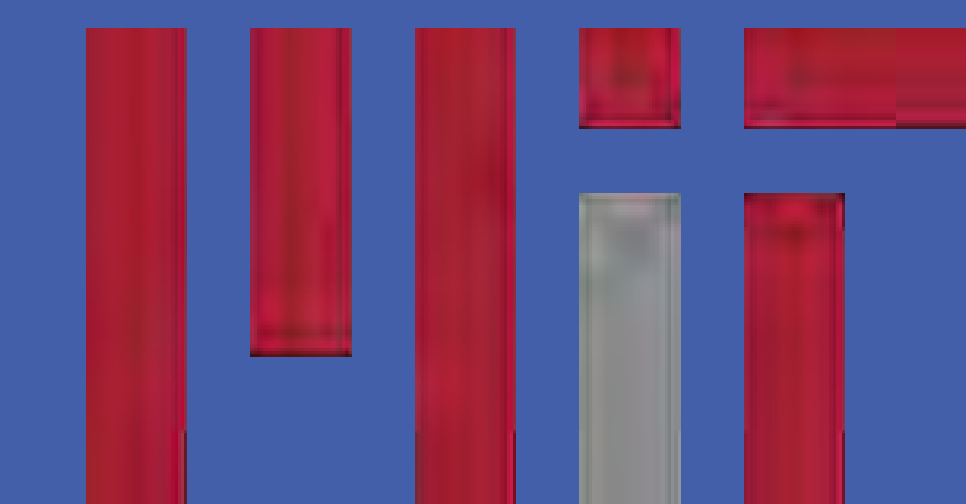


# Etch Pit Density Analysis of Ternary III-V Semiconductor Heterostructures

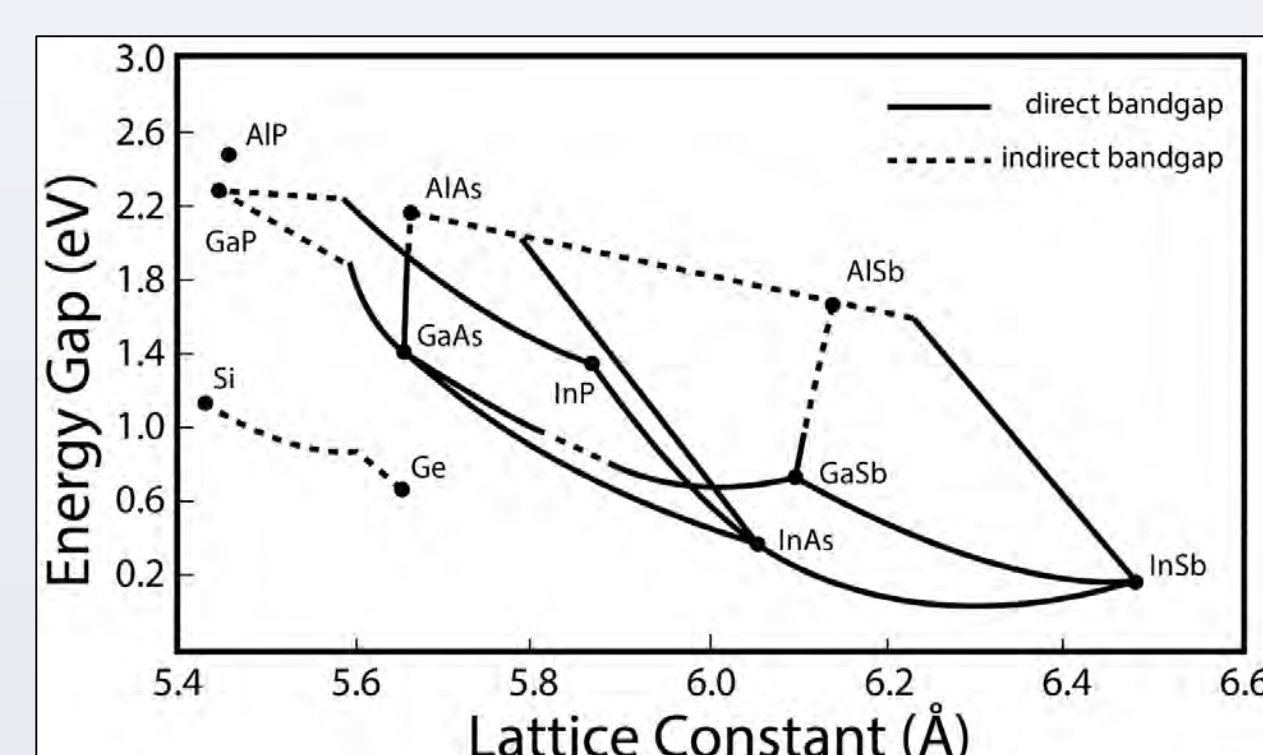


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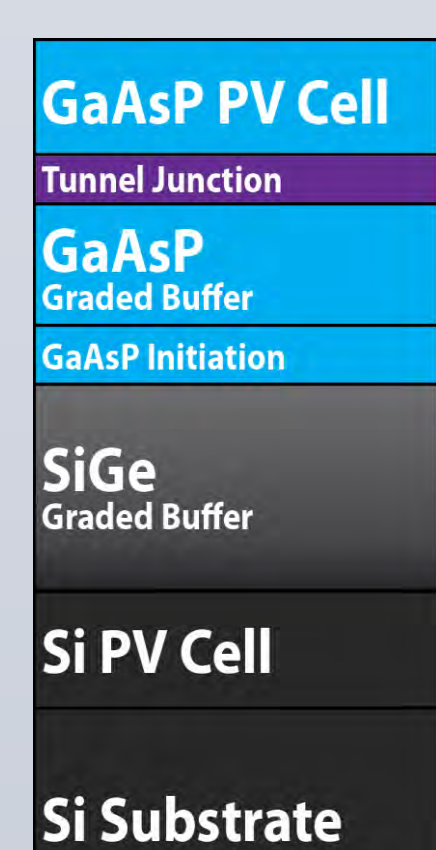
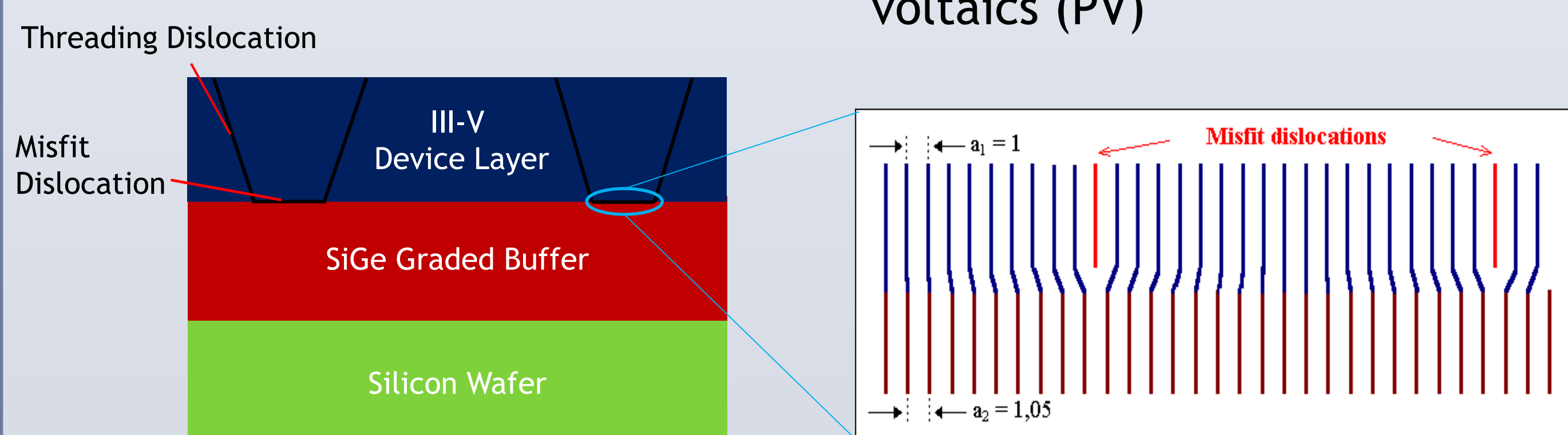
<sup>2</sup> Department of Materials Science and Engineering, Massachusetts Institute of Technology

## Introduction

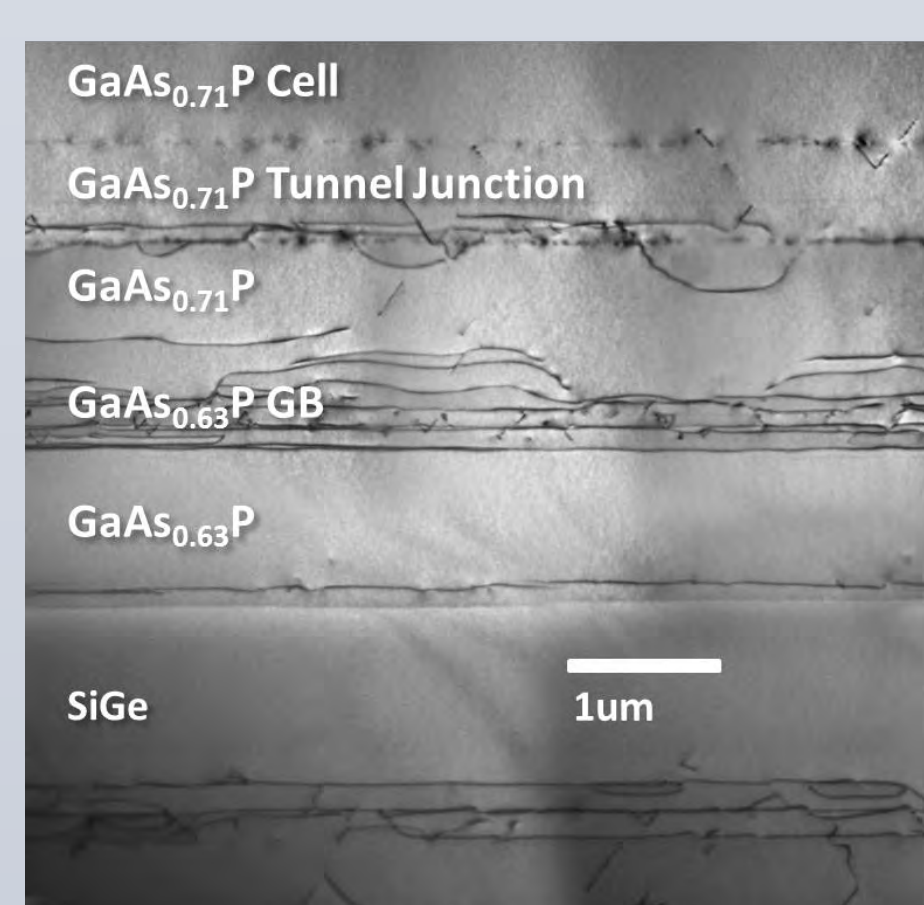


- Ternary III-V materials have numerous applications in electronics due to their variable energy structures
- Lattice constant differences between layers can cause strain that results in misfit dislocations at layer interfaces and threading dislocations in crystal bulk
- These dislocations can detrimentally affect device performance, especially in photovoltaics (PV)

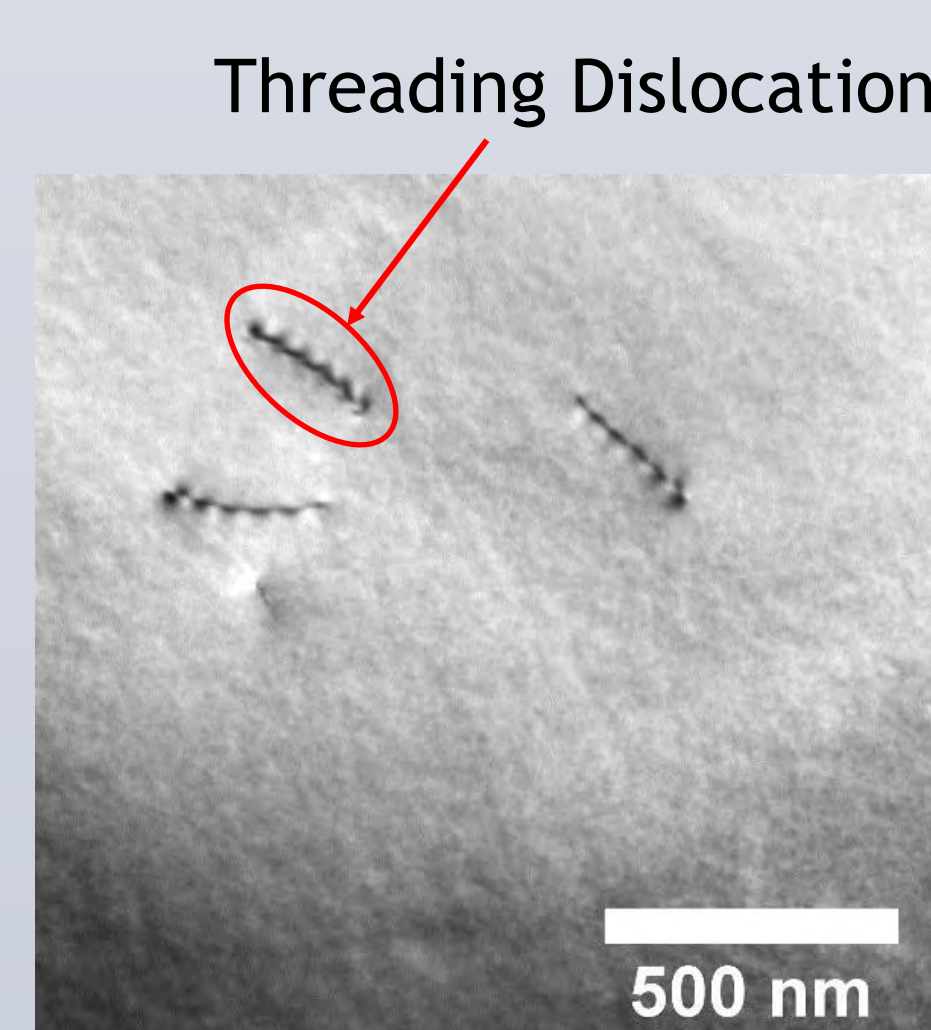
Current methods to measure dislocation densities are time-consuming and limited to only high dislocation densities



Dual Junction Cell Schematic<sup>1</sup>

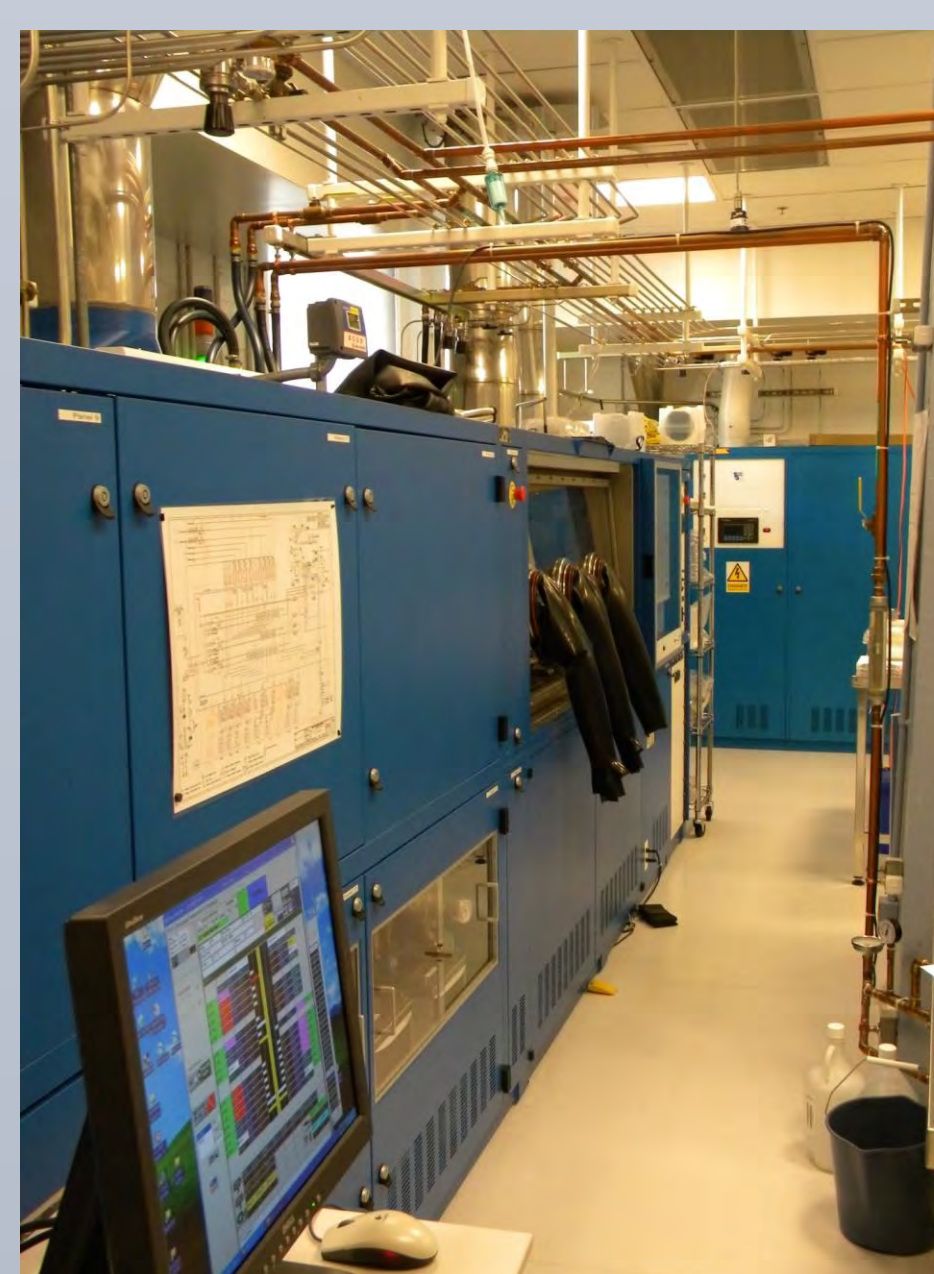


Cross Section TEM (g=(220))<sup>1</sup>



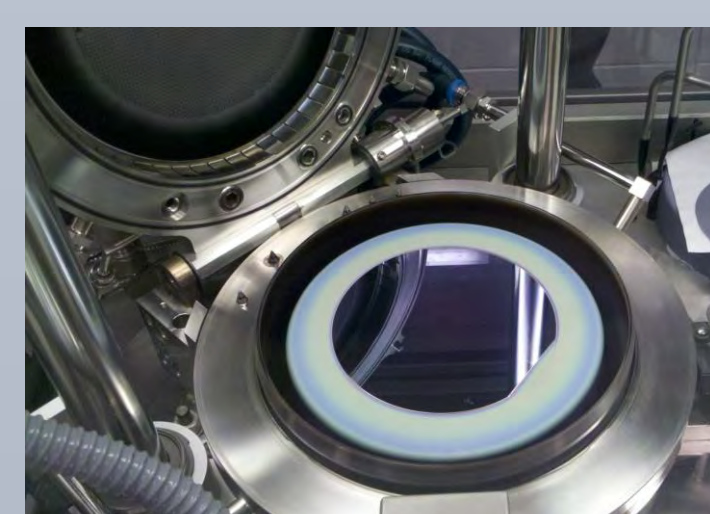
Plan view TEM (g=(220))<sup>2</sup>

## Materials



MOCVD Reactor

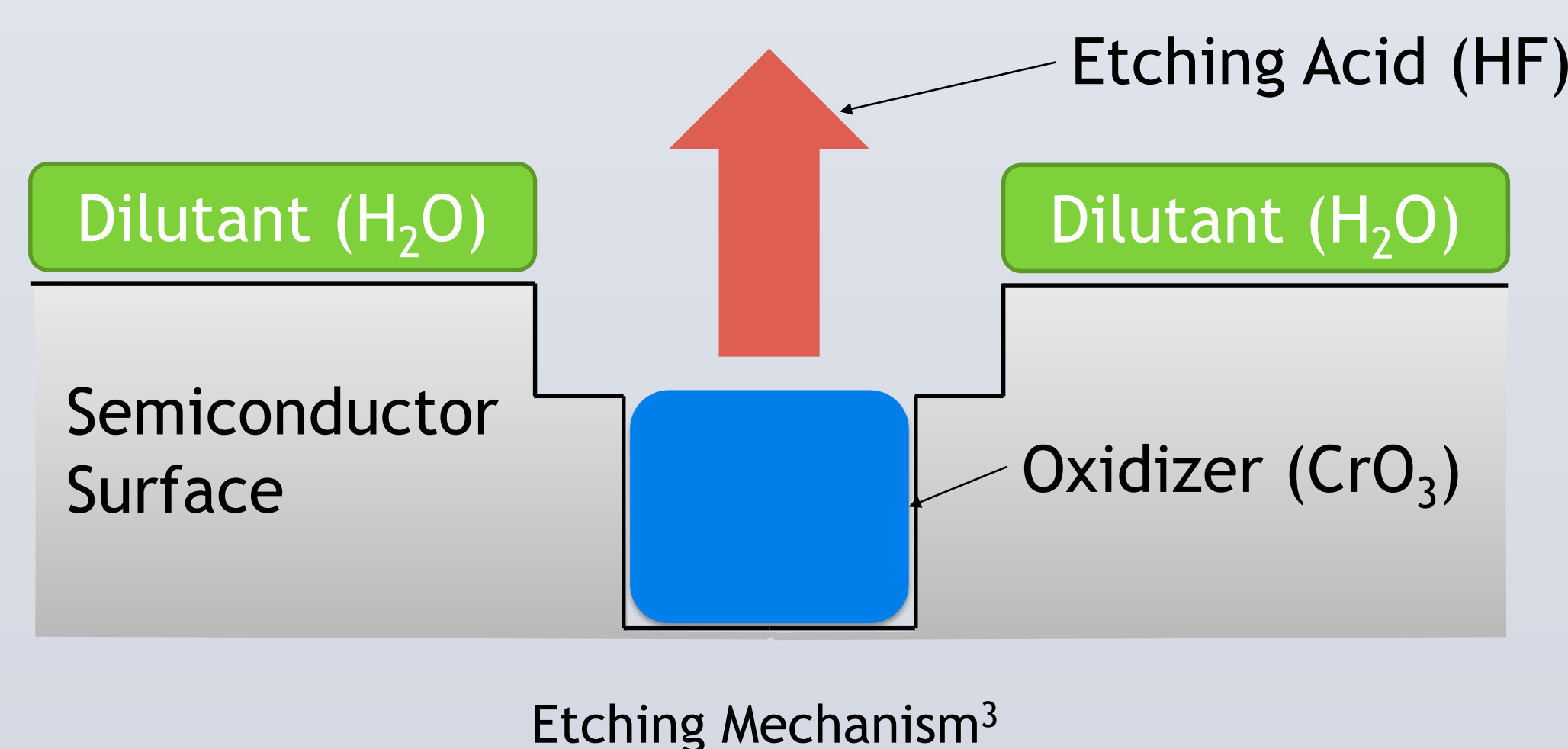
- Si<sub>x</sub>Ge<sub>1-x</sub> graded buffers grown on silicon wafers in Ultra-High Vacuum Chemical Vapor Deposition (UHVCVD) Reactor
- All III-V devices layers were grown in Aixtron Metal-Organic Chemical Vapor Deposition (MOCVD) reactor



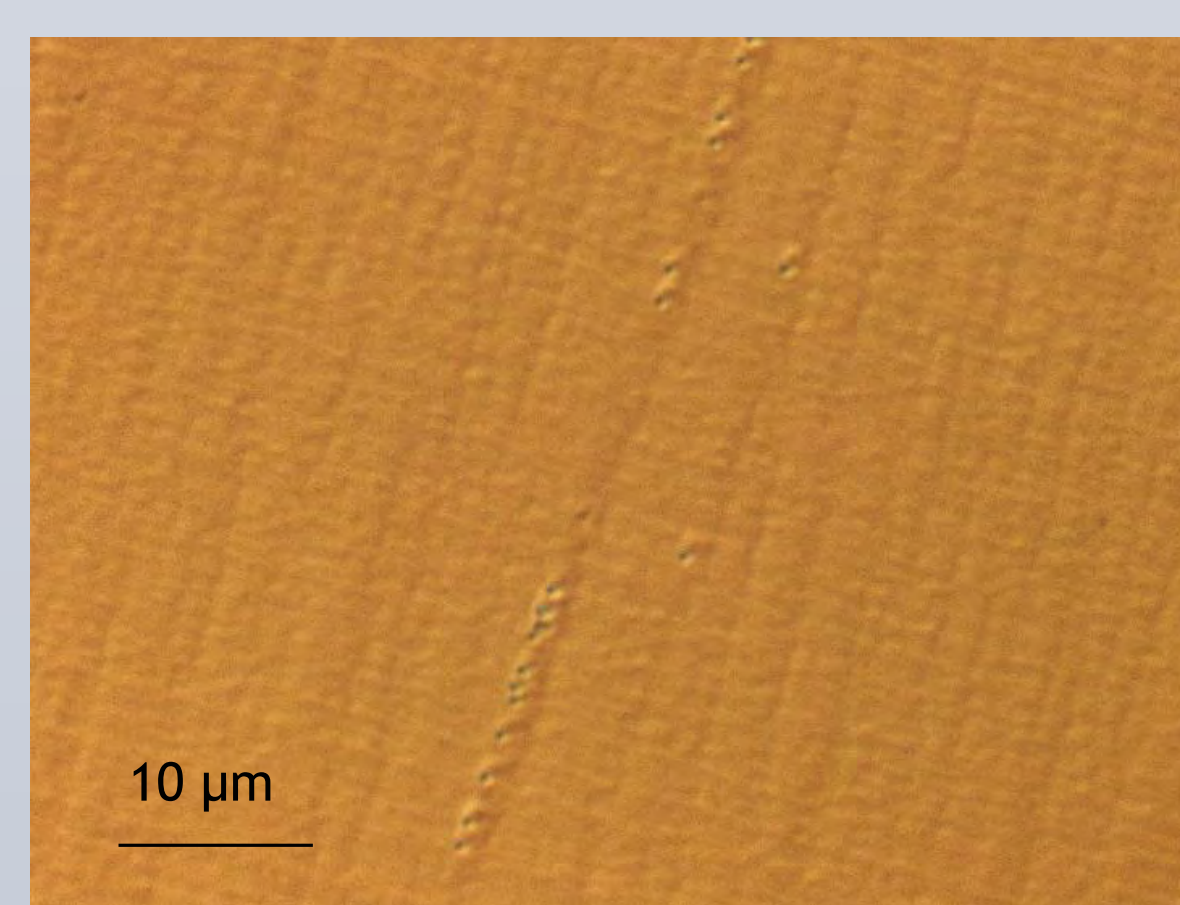
Close-Coupled Showerhead and reactor chamber of MOCVD Reactor

## Etching Methods

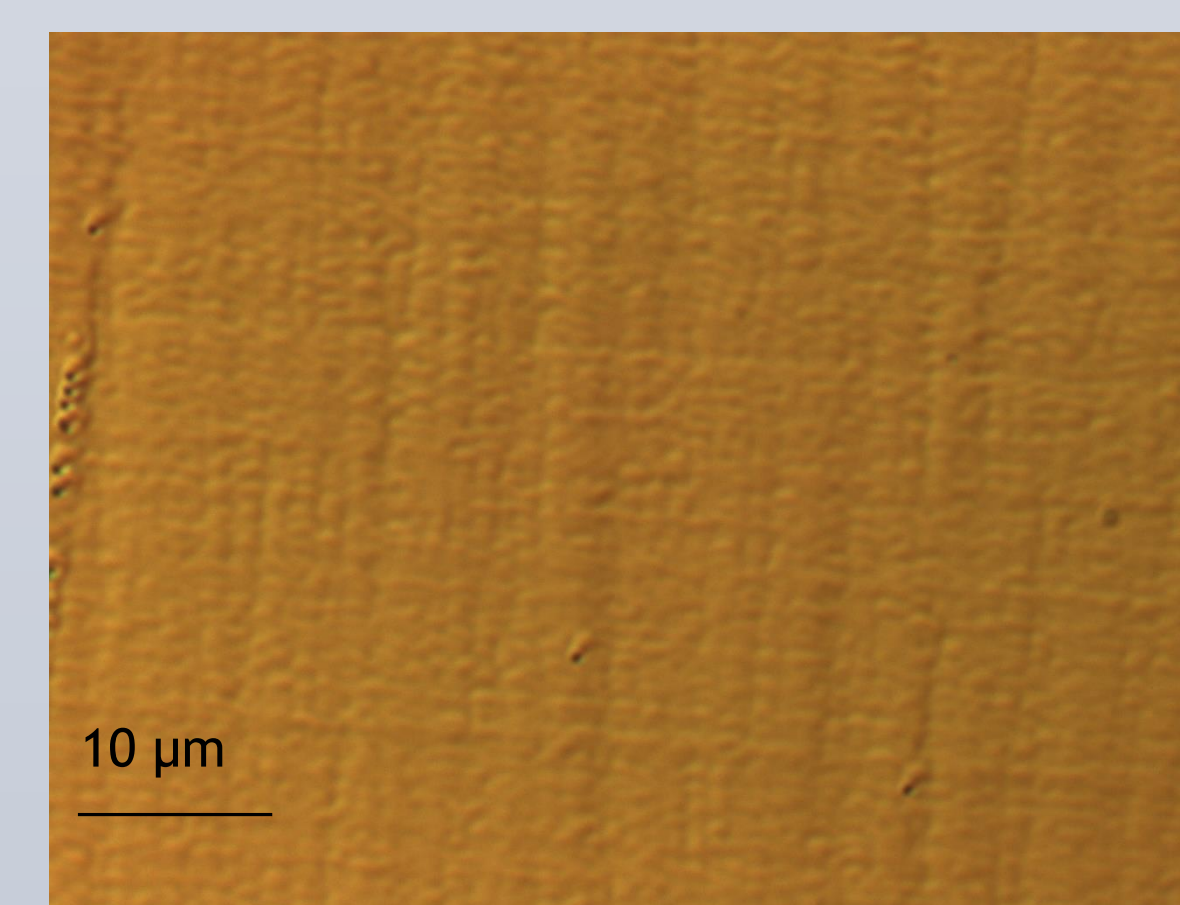
- Etch pit density (EPD) measurements allow for very quick measurements of dislocation densities over large areas of a semiconductor's surface
- Dislocation Selective Etchants (DSEs) remove material much faster at a dislocation, forming etch pits at these sites
- Concentrated HCl was used to first remove a 20nm InGaP window on the top of GaAsP samples
- Both Molten KOH and a CrO<sub>3</sub>/HF/H<sub>2</sub>O etches were examined as potential DSEs for GaAsP
- Samples were then viewed on a Nomarski microscope and etch pits were counted for density measurements



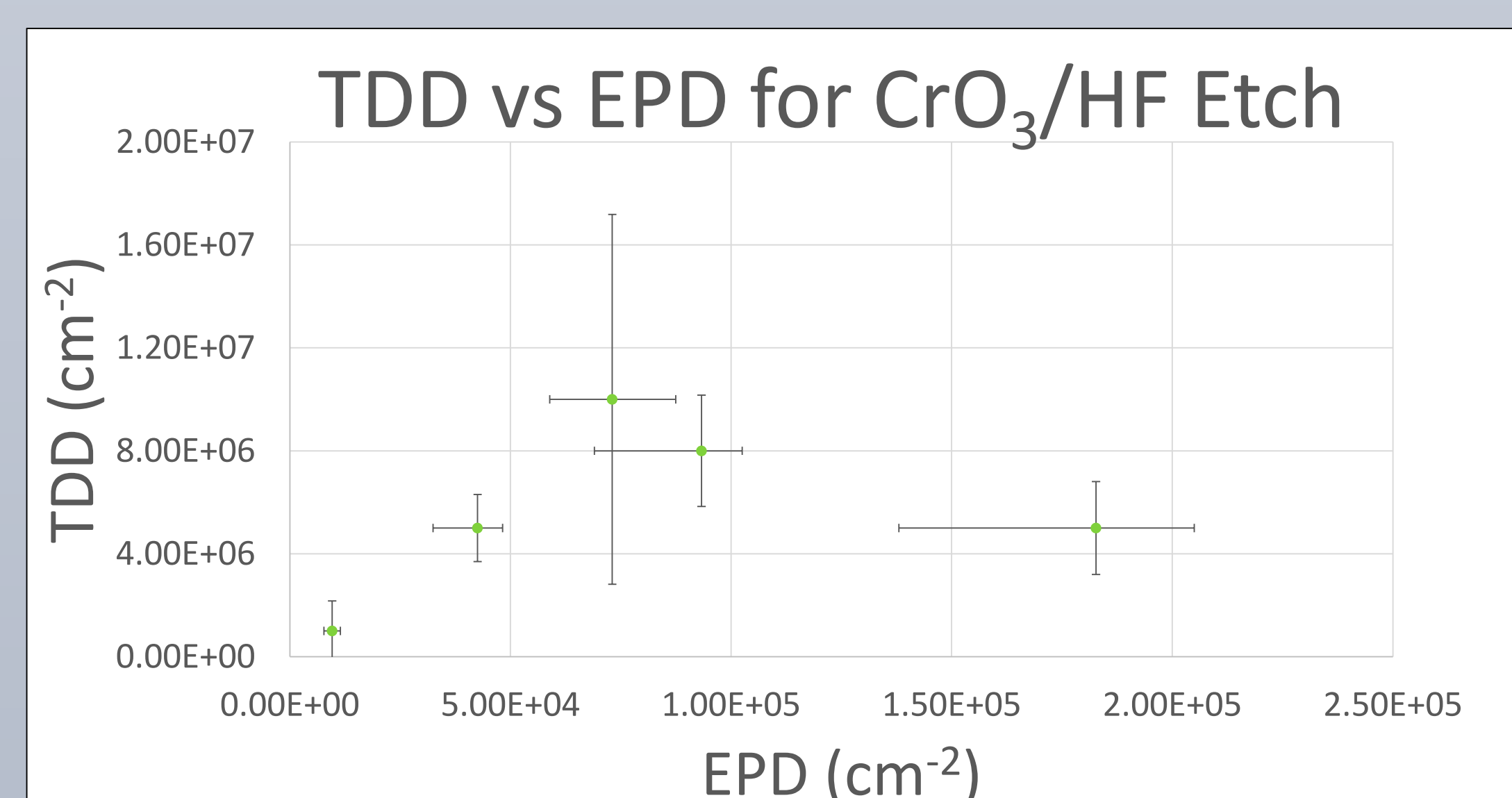
## Results



Molten KOH 1000x



CrO<sub>3</sub>/HF/H<sub>2</sub>O 1000x



## Conclusion

- Of the etches investigated, only the CrO<sub>3</sub>/HF/H<sub>2</sub>O and molten KOH etch produced etch pits
- Problems with cleanliness and contaminant deposition on sample surface made molten KOH etch data irreproducible
- CrO<sub>3</sub>/HF/H<sub>2</sub>O etch produced repeatable etch pit densities
- EPD measurements were two orders of magnitude less than threading dislocation densities measured using plan view TEM

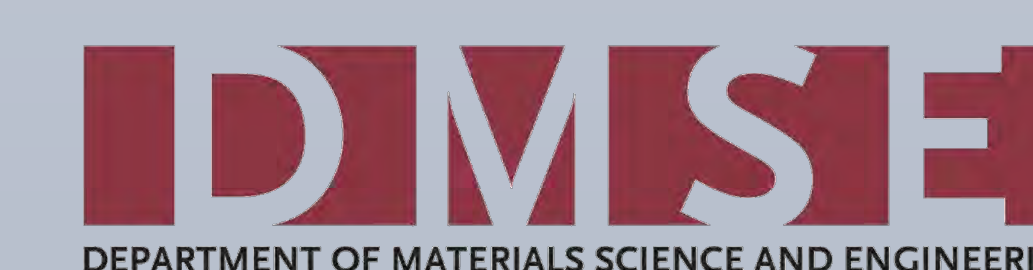
## Future Work

- Perform SEM microscopy on etch pits created in KOH and CrO<sub>3</sub>/HF/H<sub>2</sub>O etch to examine their crystallographic specificity and shape
- Adjust variables, such as temperature, and modify the procedure of the KOH etch in order to produce more reproducible results
- Investigate other etching chemistries for GaAsP as well as other ternary III-V compounds such as InGaAs and InGaSb

## References

- Sharma, Prithu. "Integration of GaAsP Alloys on SiGe Virtual Substrates for Si-Based Dual-Junction Solar Cells" (PhD Thesis). 14 May 2014
- Heidelberger, Christopher. "GaAs<sub>x</sub>P<sub>1-x</sub>/In<sub>y</sub>Ga<sub>1-y</sub>P Heterostructures for Integrated III-V on Si Microelectronics" 10 February 2014. Thesis Area Exam
- Lai, Jessica. "Etch-Pit Density Survey of Germanium Silicon Graded Allots on Silicon" June 1999.

## Acknowledgements



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