# **Atomically Precise Graphene Nanoribbons** (GNRs) Berkelev



Anthony Brandon Jr.<sup>1</sup>, Juan Pablo Llinás<sup>2</sup>, Byung Yong Choi, Ph.D.<sup>2</sup>, Jeffrey Bokor, Ph.D.<sup>2</sup> <sup>1</sup>Howard University, <sup>2</sup>University of California, Berkeley

2015 UC Berkeley-Historically Black Colleges and Universities Pathways to the Science and Engineering Doctorate Program (UCB-HBCU)

#### Abstract

**UNIVERSITY OF CALIFORNIA** 

According to Moore's law, the density of transistors in a computer chip should double every two years. With current silicon-based transistors, the probability of keeping this law factual is on a downward slope unless an alternative is found. Atomically precise graphene nanoribbons (GNRs) have the potential to become the replacement for silicon transistors due to their predicted superior electrical qualities and ultrathin body. We fabricated GNR field effect transistors (FETs) using atomically precise GNRs with 20 nm channel lengths and an  $I_{on}/I_{off}$  ratio in which the  $I_{on}$  was 1000 times higher than the I<sub>off</sub> current. The GNRs are environmentally sensitive when characterized due to adsorbed oxygen at the contact.

Research Objectives		Results		
Integrate GNRs into electronic devices	1		Raman spectra of fabricated GNRs after transfer	
Find an alternative to silicon Field Effect Transistor (FET)		D G	onto oxide substrate and device fabrication. Ideal	

using GNRs

- Improve performance and yield of GNR devices
- Alternative to carbon nanotubes (CNT) as high-performing semiconducting channel material
- Measure and characterize their electronic responses through transport and optical spectroscopy

## Fabrication

The bottom-up process to prepare atomically precise GNRFETs consists of GNR synthesis on metals and subsequent transfer to insulating substrates. Using E-beam lithography and liftoff, Pd pads are are placed onto the substrate with gaps ranging from 15-25 nm to conduct transport measurements. These GNRs are exactly 7 carbon atoms across with a band gap of 2.5 eV and a width of 0.74 nm.







Ambient

graphene has two main peaks in its Raman spectrum, which are the G and 2D peak. The D peak is a disorder peak.

This graph represents the electrical characteristics of multiple GNR transistors measured in air. The smaller figure is the previous work obtained by our predecessors. The low-yield of our devices is a result of the random position and orientation of the ribbons. GNRs contacted with Pd source/drain metals in air have P-type conduction. Compared to the average Si transistors, these I<sub>on</sub> are low. This has to do mainly with the height of the Schottky barrier, which is affected by the metal itself, water, interaction at the interface, contamination, absorbed oxygen, and the measurement environment. The  $I_{off}$ represents how much energy is being wasted in the off state.

Ambient

OFFICE OF THE PRESIDENT



We used three different transfer methods which include the HF transfer, HCL transfer, and powder transfer. It is extremely critical to have an effective transfer because the more GNRs transferred, the more chances we have to measure working devices.



Above is a SEM image of source/drain after ebeam and lift-off process. The GNRs can be as long as 25-40 nm. Therefore, it is crucial to contact ribbons at both the source and the drain with a very narrow and precise gap.

### References

[1] P. B. Bennett, Bottom-up graphene field effect transistors. Appl. Phys. Lett., vol. 103, 253114 (2013)



High Vacuum

Device behavior switches from P-type when measured in air to N-type when measured under cryogenic conditions in the probe station. The device then switches back to P-type when measured in ambient conditions. This was heavily weighed upon by the Schottkey barrier, which is illustrated below.



### **Future Work**

- Try GNRs with the following characteristics: N=9, 0.9 nm width, and 1.9 eV band gap; N=13, 1.3 nm width, and 2.5 eV band gap
- Make shorter gaps precise enough to not short on every measurement
- Try different contact metals other than Pd

Increase yield to about 20 devices per chip









anthony.brandon@bison.howard.edu

The author would like to thank Dr. Bokor and his research group



**Support Information** UNIVERSITY of CALIFORNIA This work was funded by the University of California Office of the President.